



March 2007

- Ovenized quartz crystal high precision square wave generator with a Sine Wave output.
- Tube packaging is available.

- 10 to 20 MHZ
- Full Size Thru-Hole DIP package
- Electronic Frequency Control (EFC) optional
- Good phase noise characteristics

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 6.2 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020C

Second Level Interconnect code: e1

Absolute Maximum Ratings:

Parameter	Unit
V _{cc} Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V

Reliability: Environmental Compliance

Parameter	Condition
Vibration	10 to 2000 Hz / 10 g
Shock	2000 g, 0.3 mS, ½ sine
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A



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art Numb	er:						
SM4048052	G	G	010	040	- 20.00M	-XX	
							Internal code or blank
							Frequency MHZ (standards Shown) 10.000 12.800 16.000 16.384 19.440 20.000 32.768 40.000
							Electronic Frequency Control 000 = No EFC 020 = ± 2.0 ppm minimum 040 = ± 4.0 ppm minimum 150 = ± 15.0 ppm minimum 999 = ± 4.0 ppm with 0 to 10K ohm
							Frequency Stability 003 = ±25 ppb for 0°C to 60°C 008 = ±75 ppb for 0°C to 60°C 005 = ±50 ppb for -20°C to 70°C 015 = ±150 ppb for -20°C to 70°C 010 = ±100 ppb for -40°C to 85°C 025 = ±250 ppb for -40°C to 85°C
							Upper Operating Temperature C = 50°C F = 65°C J = 80°C D = 55°C G = 70°C K = 85°C E = 60°C H = 75°C L = 90°C
							Lower Operating Temperature C = 0°C
							Series Model

Part Marking:

Code

PLE Where: c = N for no EFC, R for resistor, V for voltage

OSM4050c *fff.fff* = Frequency in MHz

fff.fff M Ymda = Date code (Year Month Day plus internal code)

ymdannn nnn = Device number

Standard values are listed, consult Pletronics Inc. for other options. Specifications such as frequency stability and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

G

Codes for Date Code YMD

Code	6	7	8	9	0	1	2
Year	2006	2007	2008	2009	2010	2011	2012

Е

Montl	h .	AN	FE	3 MA	R	APR	MAY	′ JUN	JUL	AU	G	SEP	OCT	NOV	DEC
Code	1		2	3	'	4	5	6	7	8		9	Α	В	С
Day	1		2	3	4	4	5	6	7	8		9	10	11	12
Code	D		E	F	(G	Н	J	K	L		M	N	Р	R
Day	13	1	14	15	1	16	17	18	19	20		21	22	23	24
Code	T		U	٧	1	W	Χ	Y	Z				_		
Day	25	2	26	27	2	28	29	30	31						



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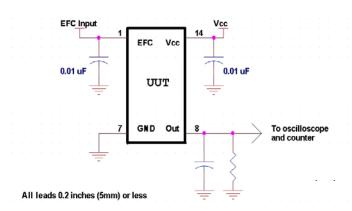
Specification for 5.00V ±0.20V over the specified temperature range

Item	Min	Max	Unit	Condition		
Frequency Range	10	20	MHZ	See list of standard fre	quencies	
Frequency Accuracy vs. Temperature	-250	250	ppb	determined by part number		
Frequency Accuracy vs. Supply	-100	+100	ppb	for Supply change of 0	.2V	
Frequency Accuracy vs. Load	-10	+10	ppb	Load change of ±10%		
Frequency Accuracy Short	-0.5	+0.5	ppb	for periods of 0.1 seco	nds to 30 seconds	
Term	-0.05	+0.05	ppb	for a period of 1 secon	d	
Aging 1st Year	-0.70	+0.70	ppm			
10 Years	-4.0	+4.0	ppm	Accumulated for 10 year	ars	
Frequency Control Voltage	-4.0	+4.0	ppm	0.5V to 5.0V, determined by part number > 47 K ohm		
(positive slope) Resistance	-4.0	+4.0	ppm	0 to 10 Kohm, determined by part number > 4.7 K ohm		
Phase Noise 1 Hz 10 Hz 100 Hz 1,000Hz		-80 -110 -135 -145	dBc/Hz			
Warmup	-	30	sec	within specification after turn on at 0°C		
Output Waveform		Sine Wav	е			
Output Level	1.0	2.0	V PP	50 ohm load	0	
	2.0	4.0	V PP	1K ohm // 5pF	See Load Circuit	
Output Harmonics	1	-10	dBc	50 ohm load		
Output Spurious	1	-70	dBc	50 ohm load		
Power Supply Current	1	110	mA	at -20°C		
	1	70	mA	at +30°C		
Warmup	1	250	mA	for 10 seconds maximu	ım	
Operating Temperature Range	-40	+85	°C	Part number defines the temperature range to meet the accuracy specification		
Storage Temperature Range	-55	+125	°C			



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Load Circuit (nominal load: 50 ohms)



ESD Rating

Model	Minimum Voltage	Conditions
Human Body Model	2000	MIL-STD-883 Method 3115
Charged Device Model	2000	JESD 22-C101

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

P/N: OHM4048052GG010040-20.00M

Customer P/N: 12345678

Qty: 1000 0510M012

(Will show OSM on label)

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect Category=e1

Max Safe Temp=250C for 10s Per Lead

Hand Solder Recommended



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mm

20.3 max

13.2 max

8.00 max

6.35

0.51

2.79

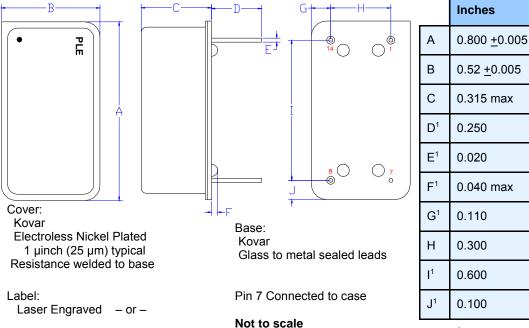
7.62

1.0 max

PCB Mounting (typical for lead free processing)

Hand soldering is recommended at 250°C maximum for 5 seconds maximum per pin

Mechanical:



	l ¹	0.600	15.24					
	J ¹	0.100	2.53					
¹ Nominal dimension								
rol voltage, depends on option ordered.								

Pin	Function	Note
1	EFC	10 K ohm to ground –OR– 0.5 to 5.0V control voltage, depends on option ordered. Use the 30% value for initial operation
7	Ground (GND)	
8	Output	
14	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.

Layout and application information

For Optimum Jitter Performance, Pletronics recommends:

Minimize air flow over the oscillator

White Kapton with Black Letters

Stabilize the power supply voltage for best performance.



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